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S60	275	(multiphase or multi-phase or multi-level) adj2 encod\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/03 12:37
S61	24	S60 and driver\$1 and bus and logic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/03 12:38
S62	0	"4571735".pn. and ((fifo) or (first adj2 in adj2 first adj2 out))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/03 14:32
S63	0	"6611247".pn. and ((fifo) or (first adj2 in adj2 first adj2 out))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/03 15:20
S64	4847	"61" and((dll) or (delay adj2 lock\$3 adj2 loop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/03 15:13
S65	4847	"61" and ((dll) or (delay adj2 lock\$3 adj2 loop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/03 15:12
S66	275	(multiphase or multi-phase or multi-level) adj2 encod\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/03 15:12
S67	10	S66 and ((dll) or (delay adj2 lock\$3 adj2 loop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/03 15:13
S68	32	S66 and ((fifo) or (first adj2 in adj2 first adj2 out))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/10/03 15:20
S69	7	(multi adj2 phase adj2 encod\$3) and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 12:18

S70	3	S69 and ((control or command) adj2 (word or signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 12:25
S71	106	(phase adj2 encod\$3) same bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 11:59
S72	59	S71 and ((control or command) adj2 (word or signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 11:59
S73	44	((multi adj2 phase) near3 lock\$3) and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:46
S74	1	(((multi adj2 phase) near3 lock\$3) or (multiphase adj2 pll)) same bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 12:23
S75	48	(((multi adj2 phase) near3 lock\$3) or (multiphase adj2 pll)) and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 12:23
S76	26	S75 and ((control or command) adj2 (word or signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 12:25
S77	7	S75 and (((control or command) adj2 (word or signal)) near3 input)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:48
S78	281	(differential adj2 dll) or (differential near3 pll) or (differential near3 (multiphase adj2 pll))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 12:32
S79	281	(differential adj2 dll) or (differential near3 pll) or (differential near3 (multiphase adj2 pll))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:38

S80	5	S79 and (tim\$3 adj2 length)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:39
S81	281	(differential adj2 dll) or (differential near3 pll) or (differential near3 (multiphase adj2 pll)) near3 (waveform adj2 tim\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:42
S82	11	S81 and (tim\$3 near3 length)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:42
S83	281	(differential adj2 dll) or (differential near3 pll) or (differential near3 (multiphase adj2 pll)) same (waveform adj2 tim\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:43
S84	281	(differential adj2 dll) or (differential near3 pll) or (differential near3 (multiphase adj2 pll)) same (encod\$3 adj2 waveform adj2 tim\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:42
S85	11	S84 and (tim\$3 near3 length)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:42
S86	0	(differential adj2 dll) same (waveform adj2 tim\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:44
S87	19	(differential adj2 dll)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:45
S88	1	S87 and (waveform adj2 tim\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:44
S89	1	S87 and (waveform near3 tim\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:44

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S90	1	375/219.ccls. and (differential adj2 dll)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:45
S91	2	375/219-288.ccls. and (differential adj2 dll)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:46
S92	2	375/327-376.ccls. and (differential adj2 dll)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:46
S93	7	375/219-288.ccls. and (((multi adj2 phase) near3 lock\$3) and bus)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:47
S94	2	S93 and (((control or command) adj2 (word or signal)) near3 input)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:48
S95	0	(bus and ((first adj2 logic) same (((multiphase adj2 lock adj2 loop) and (input near3 register)) near3 ((((control or command) adj2 (word or signal)) or data) near3 input))) and ((second adj2 logic) near3 driv\$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:52
S96	1	((device adj2 driver) same ((multiphase adj2 encod\$3 adj2 waveform) near3 amplifier) and (differential adj2 (delay adj2 lock\$3 adj2 loop) or dll)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/04/12 15:54

Page 10